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Deflection routing in slotted self-routing networks with arbitrary topology [PDF]
CY Li, PKA Wai, XC Yuan, ... - ... , 2002. ICC 2002. IEEE ..., 2002 - ieeexplore.ieee.org
Abstract—A deflection routing algorithm that can be applied to a novel self-routing address scheme for networks with arbitrary topology is proposed. The proposed deflection routing algorithm can be implemented all-optically using bitwise optical logic gates. Besides the primary output ...
Cited by 15 - Related articles - BL Direct - All 8 versions

A novel self-routing address scheme for all-optical packet-switched networks with arbitrary topologies [PDF]
XC Yuan, CKK Li, CY Li, ... - ... Technology, Journal of, 2007 - ieeexplore.ieee.org
... For a given network topology, it is hard in gen- eral to determine the most suitable regular topology for the map- ping. ... In this paper, we propose a novel self-routing address scheme that is applicable to networks with arbitrary topologies. ...
Cited by 43 - Related articles - BL Direct - All 11 versions

Labelling and implicit routing in networks
N Santoro, ... - The computer journal, 1985 - Br Computer Soc
... Efficient implicit routing algorithms are known only for networks modelled by graphs having special topologies (eg ring, hypercube). In this paper, a labelling scheme for graphs of arbitrary topology is presented, and an implicit routing algorithm is derived. ...
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... self-routing multistage interconnection network having contention-free fanout, low-loss routing, and fanin buffering to efficiently realize arbitrarily low packet loss
AV Krishnamoorthy, ... - US Patent 5,541,914, 1996 - Google Patents
... 30 1996 [54] PACKET-SWITCHED SELF-ROUTING MULTISTAGE INTERCONNECTION NETWORK HAVING CONTENTION-FREE FANOUT, LOW-LOSS ROUTING, AND FANIN BUFFERING TO EFFICIENTLY REALIZE ARBITRARILY LOW PACKET LOSS [76] Inventors: ...
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METANET: principles of an arbitrary topology LAN
Y Ofek, ... - Networking, IEEE/ACM Transactions on, 1995 - ieeexplore.ieee.org
... Abstract—The MetaNet is a scalable local area network (LAN) architecture with an arbitrary topology and a switch at each node (ie, a switch ... Following properties: 1) no packet loss due to congestion, 2) fair access to the network, 3) no deadlocks, and 4) self-routing with broadcast. ...
Cited by 78 - Related articles - All 3 versions

Deflection routing in slotted self-routing networks with arbitrary topology [PDF]
CY Li, PKA Wai, XC Yuan, ... - Selected Areas in ..., 2004 - ieeexplore.ieee.org
Abstract—A deflection routing scheme for small to medium size future all-optical networks with arbitrary topologies is proposed. The proposed scheme assumes only single-bit all-optical processing and no buffers. The primary output selection and the alternate output choices by a packet ...
Cited by 7 - Related articles - BL Direct - All 6 versions

Principle for high speed network control: congestion-and deadlock-freeness, self-routing, and a single buffer per link
Y Ofek, ... - Proceedings of the tenth annual ACM symposium ..., 1991 - portal.acm.org
... In this paper we present principles of operation for the basic control functions of a high-speed network in an arbitrary topology, we then ... and on the other hand the network access and flow control will ensure no loss, fair access to the network, no deadlocks and self-routing. ...
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A novel self-routing scheme for all-optical packet switched networks with arbitrary topology

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Efficient adaptive routing in networks of workstations with irregular topology
F Silla, M Malumbres, A Robles, P Lopez ... - ... and Architectural Support ..., 1997 - Springer
... Page 15 60 27. W. Qian and LM Ni, "Adaptive routing in irregular networks using cut-through ... 28. MD Schroeder et al., "Autonet: A high-speed, self-configuring local area network using point-to-point links," Technical Report SRC research report 59, DEC April 1990. 29 ...
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A distributed reinforcement learning scheme for network routing [PD]
M Littman ... of the 1993 international workshop on ..., 1993 - books.google.com
... Research Group, Bellcore Morristown, NJ Abstract In this paper we describe a self-adjusting algorithm ... we present an example of such an algorithm for the problem of routing packets efficiently in a communication network with an irregular topology and unpredictable ...
Cited by 112 - Related articles - Library Search - All 24 versions

Improving the efficiency of adaptive routing in networks with irregular topology [PD]
F Silla ... 1997 - computer.org
... [8] MD Schroeder et al., "Autonet: A high-speed, self- configuring local ... [9] SL Scott and G. Thorson, "The Cray T3E network: adapt- ive routing in a ... [10] F. Silla and J. Duato, "On the Use of Virtual Channels in Networks of Workstations with Irregular Topology," in Proceedings of ...
Cited by 56 - Related articles - All 10 versions

High-performance routing in networks of workstations with irregular topology
F Silla ... - Parallel and Distributed Systems, IEEE ..., 2000 - ieeexplore.ieee.org
... In this paper, we propose two general methodologies for the design of adaptive routing algorithms for networks with irregular topology ... In this section, we propose two general methodologies for the design of adaptive routing algorithms for networks with irregular topology ...
Cited by 67 - Related articles - BL Direct - All 11 versions

A new methodology to compute deadlock-free routing tables for irregular networks
J Sancho, A Robles ... - Network-Based Parallel Computing ..., 2000 - Springer
... Many NOWs are ar- ranged as a switch-based network with irregular topology, which makes routing and deadlock avoidance quite complicated. Current proposals use the up */down* routing algorithm to remove cyclic dependencies between channels and avoid deadlock ...
Cited by 47 - Related articles - All 9 versions

On the use of virtual channels in networks of workstations with irregular topology
F Silla ... - Parallel Computer Routing and Communication, 1998 - Springer
... In order to make this paper self-contained, we briefly present the routing algorithm used in the simulations carried ... The resulting flow control protocol has been evaluated by simulation on networks with irregular topology, using the fully adaptive routing algorithm previously ...
Cited by 20 - Related articles - BL Direct - All 8 versions

Multicast on irregular switch-based networks with wormhole routing [PD]
S Kesavan, K Bondalapati ... - hpca, 1997 - computer.org
... Switch-based networks are typically built for supporting irregular topology. Such irregularity provides flexibility to design scalable systems with incre- mental expansion capability. The irregularity also makes the routing on such systems quite complicated ...
Cited by 53 - Related articles - All 12 versions

A distributed self spreading algorithm for mobile wireless sensor networks [PD]
N Heo ... - Vehicular Technology Conference, 2003 ..., 2003 - ieeexplore.ieee.org
... hoc networks has been focused on issues such as the development of routing protocols and ... Our self deployment algorithm can be classified as a blanket coverage problem according to ... sensor nodes spend energy more evenly through the WSN than an irregular topology does ...

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Method and system for dual-network address utilization
MS Borella ... - US Patent 6,708,219, 2004 - Google Patents
... 58 SELECT A PORTION OF THE **DUAL** PROTOCOL STACK FOR USE WITH Y-BIT ... Y-BIT NETWORK **ADDRESS** MAY INCLUDE A DNAT/RSIP Y-BIT NETWORK **ADDRESS** 108 ADD A ... PACKET 110 TRANSMIT THE DATA PACKET TO THE VIRTUAL TUNNEL **GATEWAY** VIA A ...

IPv6 integration and coexistence strategies for next-generation networks
M Tatipamula, P Grossetete ... - Communications ..., 2004 - ieeexplore.ieee.org
... communicate with IPv4-only Web servers or dual-stack), but one drawback — well known from NAT users — is the need for dedicated application layered gateways (ALGs) when an application payload embeds an IP **address**. The SOCKS-based IPv4/IPv6 **gateway** mecha- nism ...

VGSN: a gateway approach to interconnect UMTS/WLAN networks
SL Tsao ... - ..., The 13th IEEE International Symposium on, 2002 - ieeexplore.ieee.org
... (2). Users with the dual mode devices subscript a data service of one network can use ... (3) Applications use only one IP **address** to access Internet. ... The logical entity can be either implemented as a separated node or integrated with **Gateway** in WLAN or SGSN or GGSN nodes. ...

Apparatus and method for providing a secure gateway for communication and data exchanges between networks
HT Vu - US Patent 5,623,601, 1997 - Google Patents
... Users can only access public services for which an appli- cation **gateway** has been installed on the dual homed gate- way ... all communications packets that are encapsulated with a hardware destina- tion **address** that matches the device **address** of the **gateway**; b) determining ...

Marketplace-the IPv6 transition
T Dunn - Internet Computing, IEEE, 2002 - ieeexplore.ieee.org
... The 6to4 specification states that the 32 bits after the IPv6 prefix denote the IPv4 **address** of the **gateway** for a given network. The 6to4 scenario does pre- sent some security concerns because you usually can't set filters to block packets from unauthorized sources. **Dual** stack. ...

Router for which a logical network address which is not unique to the router is the gateway address in default routing table entries
SE Hrastar ... - US Patent App. 09/767,926, 2001 - Google Patents
... 9,2001 (54) ROUTER FOR WHICH A LOGICAL NETWORK **ADDRESS** WHICH IS NOT UNIQUE TO THE ROUTER IS THE **GATEWAY ADDRESS** IN DEFAULT ROUTING TABLE ENTRIES (76) Inventors: Scott E. Hrastar, Duluth, GA (US); George Horken Smith, Atlanta, GA (US ...

Improving gateway performance with a routing-table cache
DC Feldmeier ... - ... Seventh Annual Joint Conference of the ..., 1988 - ieeexplore.ieee.org
... Any **gateway** that is not directly connected to the destination must send the packet to the next **gateway** in the chain from source to destination; the **address** of this next **gateway** is the next-hop **address**. The **gateway** examines the ...

[HTML] RSIP: Address sharing with end-to-end security
MS Borella ... - Special Workshop on Intelligence ... - usenix.org

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Address translation mechanism for a high-performance network switch
M Marcelo, T Ferrell ... - US Patent 5,740,171, 1998 - Google Patents
... of a high performance effect forwarding decisions for the frame based on port switch is a ... Performance degradation may be accept a destination address as an input and deliver a port further ... ports (ie, store unit is required (eg, on the order to terabytes) to "dual-homed" connection ...
Cited by 286 - Related articles - All 2 versions

Alerting system with dual-address memory
TF Siodorski - US Patent 4,010,461, 1977 - Google Patents
... Thus, when the page memory 32 is via line 29 by operating a momentary-contact switch interrogated, by operating the push-button 28 as ... mode which is determined by the then-existing signal gate to terminate operation of the alert signal 20 state of the dual-address memory 13 ...
Cited by 15 - Related articles - All 2 versions

Dual mode SIMD/MIMD processor providing reuse of MIMD instruction memories as data memories when operating in SIMD mode
NK Ing-Simmons, RM Gurley, RJ Gove ... - US Patent [illegible], 1993 - Google Patents
... 24.1993 [54] DUAL MODE SIMD/MIMD PROCESSOR PROVIDING REUSE OF MIMD INSTRUCTION MEMORIES AS DATA MEMORIES WHEN OPERATING IN S1MD MODE [75] Inventors Nicholas K. Ing ... A crossbar switch serves to establish the processor memory links. ...
Cited by 221 - Related articles - All 2 versions

ATM local area network switch with dual queues
RC Lauer, A Ghosh, JH Howard, H Kondoh ... - US Patent ..., 1998 - Google Patents
... REQ ADDRESS C)UTPUT, 49 EXPANSION OUTPUT Page 7. US Patent 5,732,087 Mar. 24, 1998 Sheet 6 of 6 CM- oo ¬o CO oo CO Page 8. 5,732,087 1 2 ATM LOCAL AREA NETWORK SWITCH from incoming to outgoing network links Traffic is handled WITH DUAL QUEUES ...
Cited by 21 - Related articles - All 2 versions

Alerting system with dual-address memory
TF Siodorski - US Patent 4,145,684, 1979 - Google Patents
... A second operation of this switch terminates the recalled alert signal and resets the receiver into condition for receiving another calling ... NATURE OF THE INVENTION The present invention provides a paging receiver having both page memory and dual-address capabilities that ...
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Dual address RAM
RH Ong, PC Economopoulos ... - US Patent 4,628,489, 1986 - Google Patents
... 4,628,489 1 2 cither 6 is representative of a plurality of write amplifi- DUAL ADDRESS RAM ers ... the same memory cell in each of the several blocks will be selected for any given address. ... is connected, in parallel, to the base electrodes of a plurality of sense switch transistors 88 ...
Cited by 25 - Related articles - All 2 versions

Telephone switch dual monitors
PE Brockman, JI Ayers ... - US Patent 5,592,530, 1997 - Google Patents
... [ii] Patent Number [45] Date of Patent 5,592,530 Jan. 7, 1997 [54] TELEPHONE SWITCH DUAL MONITORS [76] Inventors Pierce E. Brockman, Garland; John I. Ayers; Gary S. Ruwaldt, both of Plano, all of Tex. [73] Assignee Inet, Inc., Plano, Tex. [21] Appl. ...
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Method and system for dual-network address utilization
MC Bibiella ... - US Patent 6,795,218, 2004 - Google Patents
... networks Dual address allocation of IPv4 and/or IPv6 network addresses is pro- vided

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[Method and system for dual-network address utilization](#)
MS Borella ... - US Patent 6,708,219, 2004 - Google Patents
... More specifically, it relates to a method and system for dual network address utilization. ...
Computational burdens placed on a network address translation router may be significant and
degrade network performance, especially if several network address translation ...
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[High bandwidth PCI to packet switched router bridge having minimized memory latency](#)
SC Miller - US Patent 5,915,104, 1999 - Google Patents
... This bit is compared against address bit 57 in selecting or clearing the buffers ... The Bridge ASIC
supports a dual ring arbitration scheme, allow- ing PCI devices to be either real ... transfers must
be broken up into quarter cache transfers (on the Switched packet router interconnect) or ...
Cited by 85 - Related articles - All 2 versions

[Data router backup feature](#)
GC Hess - US Patent 5,835,696, 1998 - Google Patents
... LANs 15 and 16 may each be the well-known Ethernet Local Area Network (LAN) and routers
10-1 and 10-2 each may be, for example, the model 7000 dual ported router available from Cisco ...
Thus, port 12-1 monitors LAN 16 for a message bearing the address of router 10-1 ...
Cited by 130 - Related articles - All 2 versions

[Method for assigning a dual IP address to a workstation attached on an IP data transmission network](#)
G Gupta ... - US Patent 6,684,243, 2004 - Google Patents
... SERVER REQUESTER AUTHENTICATION AUTHORIZATION OF USER ACCESS ANSWER
REQUESTING SERVER UPDATING OF HOME ROUTER UPDATING OF USER PROXY FIG.
7 Page 7. US 6,684,243 B1 METHOD FOR ASSIGNING A DUAL IP ADDRESS TO A ...
Cited by 15 - Related articles - All 2 versions

[A proposal of dual Zipfian model for describing HTTP access trends and its application to address cache design](#)
M Aida, N Takahashi ... - IEICE Transactions on ..., 1998 - search.ieice.org
... Summary: This paper proposes the Dual Zipfian Model addressing how to describe HTTP
access trends in large-scale data communication networks, and discusses how to design
the capacity of address cache tables in an edge router of the networks. ...
Cited by 16 - Related articles - Cached - BL Direct - All 3 versions

[IPv6 integration and coexistence strategies for next-generation networks](#)
M Tatipamula, P Grossetete ... - Communications ..., 2004 - ieeexplore.ieee.org
... GRE tunnel regular communication tunnel technique only. Management overhead, address.
GRE tunnel implementation is rarely available on hosts. Dual stack router. Tunnel endpoints
can be secured using IPv4 IPsec Required with IS-IS for IPv6 is configured over a tunnel ...
Cited by 46 - Related articles - BL Direct - All 3 versions

[MIDI address converter and router](#)
BU Stewart - US Patent 4,777,857, 1988 - Google Patents
... The amount to be added or subtracted to a MIDI address is input to the Adder as ... be loaded into
the transmitter buffer of the UART 55 which begins transmission to the router at the ... The toggle
flip flops used in the Controller may be improvised from 74HC393 dual binary counters. ...
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[iDeal: Inter-router dual-function energy and area-efficient links for network-on-chip (NoC) architectures](#)